

CLAIMS

What is claimed:

- 1 1. A magnetic random access memory device comprising:
2 a plurality of magnetic memory elements;
3 a sense line coupled to the plurality of magnetic memory elements for sensing a
4 magnetic orientation of at least one of the plurality of magnetic memory elements
5 wherein the sense line includes a first via and a second via; and
6 wherein the sense line is utilized to thermally assist in switching a magnetic
7 orientation of at least one of the plurality of magnetic memory elements.
- 1 2. The device of claim 1 wherein each of the plurality of magnetic memory
2 elements comprises a spin dependent tunnel junctions.
- 1 3. The device of claim 1 wherein each of the plurality of magnetic memory
2 elements comprises a giant magnetoresistive device.
- 1 4. The device of claim 1 wherein the sense line comprises semiconductor material.
- 1 5. The device of claim 1 wherein each of the plurality of magnetic memory
2 elements further comprises at least one write conductor.
- 1 6. The device of claim 1 wherein each of the plurality of magnetic memory
2 elements includes a free layer and switching a magnetic orientation of at least one of the
3 plurality of magnetic memory elements comprises switching a magnetic orientation of

4 the free layer.

1 7. The device of claim 1 further comprising:

2 a current source coupled to the sense line wherein utilizing the sense line to
3 thermally assist in switching a magnetic orientation of at least one of the plurality of
4 magnetic memory elements further comprises utilizing the current source to provide a
5 current from the first via to the second via wherein the current heats at least one of the
6 plurality of magnetic memory elements.

1 8. The device of claim 4 wherein the semiconductor material comprises at least one
2 of Pt, SiC, Si or C material.

1 9. The device of claim 5 wherein the at least one write conductor comprises two
2 write conductors wherein the two write conductors are utilized to switch the magnetic
3 orientation of at least one of the plurality of magnetic memory elements.

1 10. The device of claim 5 wherein the at least one write conductor comprises only
2 one write conductor wherein the only one write conductor is utilized to switch the
3 magnetic orientation at least one of the plurality of magnetic memory elements.

1 11. The device of claim 6 wherein the sense line is above the free layer.

1 12. The device of claim 6 wherein the sense line is below the free layer.

1 13. The device 10 wherein the only one write conductor is positioned orthogonal to
2 the sense line.

1 14. A method of switching a magnetic orientation of at least one of a plurality of
2 magnetic memory elements in a magnetic random access device comprising:
3 coupling a sense line to the at least one of the plurality of magnetic memory
4 elements wherein the sense line includes a first via and a second via; and
5 utilizing the sense line to thermally assist in switching a magnetic orientation of
6 at least one of the plurality of magnetic memory elements.

1 15. The method of claim 14 wherein each of the plurality of magnetic memory
2 elements comprises a spin dependent tunneling junction.

1 16. The method of claim 14 wherein each of the plurality of magnetic memory
2 elements comprises a giant magnetoresistive device.

1 17. The method of claim 14 wherein the sense line comprises a semiconductor
2 material.

1 18. The method of claim 14 wherein each of the plurality of magnetic memory
2 elements includes a free layer and switching a magnetic orientation of at least one of the
3 plurality of magnetic memory elements comprises switching a magnetic orientation of
4 the free layer.

1 19. The method of claim 14 wherein utilizing the sense line to thermally assist in
2 switching a magnetic orientation of at least one of the plurality of magnetic memory
3 elements further comprising:

4 coupling a current source to the sense line; and

5 utilizing the sense line to heat at least one of the plurality of magnetic memory
6 elements.

1 20. The method of claim 14 wherein each of the plurality of magnetic memory
2 elements further comprises at least one write conductor.

1 21. The method of claim 17 wherein the semiconductor material comprises at least
2 one of Pt, SiC, Si or C material.

1 22. The method of claim 18 wherein the sense line is above the free layer.

1 23. The method of claim 18 wherein the sense line is below the free layer.

1 24. The method of claim 19 wherein utilizing the sense line to heat at least one of the
2 plurality of magnetic memory elements further comprises:

3 utilizing the current source to provide a current from the first via to the second via
4 wherein the current heats at least one of the plurality of magnetic memory elements.

1 25. The method of claim 20 wherein the at least one write conductor comprises two
2 write conductors wherein the two write conductors are utilized to switch the magnetic

orientation of the magnetic memory elements.

26. A computer system comprising:

a processor;

an interface module coupled to the processor; and

a magnetic random access memory device coupled to the interface module

wherein the magnetic random access memory device includes a plurality of magnetic

tunnel junctions and a sense line coupled to the plurality of magnetic tunnel junctions

wherein the sense line includes a first via and a second via, the sense line being capable

of thermally assisting in switching a magnetic orientation of at least one of the plurality

of magnetic tunnel junctions.

27. The computer system of claim 26 wherein the magnetic random access memory device further comprises:

a current source coupled to the sense line wherein utilizing the sense line to thermally assist in switching a magnetic orientation of at least one of the plurality of magnetic tunnel junctions further comprises utilizing the current source to provide a current from the first via to the second via wherein the current heats at least one of the plurality of magnetic tunnel junctions.

28. The computer system of claim 27 wherein each of the plurality of magnetic tunnel junctions further comprises at least one write conductor.

29. The computer system of claim 28 wherein the at least one write conductor

- 2 comprises two write conductors wherein the two write conductors are utilized to switch
- 3 the magnetic orientation of the magnetic tunnel junctions.